

Solenoid drive substrate

电磁螺线管驱动板

TSD-R20-2B 驱动

■特点

- 1、正转与反转的通电时间可以在1ms至63ms间任意设定, 出厂时设置为(T1=14 T2=14)
- 2、触发输入回路由光学耦合器绝缘, 因为电磁线圈的电源为“V1、V2”, 电路的电源为完全独立的“VCC”, 所以它可以容易的与周边回路建立联系。
- 3、由于我们使用的是“C-MOS-IC”计数/逻辑线路, 所以他们有以下特点, 例如功耗低(VCC-GND2), 杂音低, 回路电源可调电压范围广
- 4、在驱动板的两面均涂上了变性聚丙烯纤维材料, 防潮、抗震、防漏电性能相当令人满意

■规格

额定电压

- 1、电磁线圈驱动电源(H) V1-GND1:
DC12V~DC48V
- 2、电磁线圈驱动电源(L) V2-GND1:
DC12V~DC48V (V1>V2)
- 3、回路电源Vcc-GND2:DC5V±10%

回路电源电流

- 1、回路电源电流Icc2:30mA以下(Vcc-GND2)

额定输出电流

- 1、电磁线圈输出电流(在连续脉冲输入时动作)
V1, V2=48V时:1.2A以内(Ra=44欧姆)
V1, V2=24V时:2.0A以内(Ra=12欧姆)
V1, V2=12V时:1.2A以内
- 2、电磁线圈输出电流(连续保持动作)
V2=12~48V:1.2A以内

触发输入

- 1、高位输入电压 V_{IH}: DC12V~DC48V
(DC50VMAX)
- 2、低位输入电压 V_{IL}: DC0V~DC1.2V

■Features

- ① It is possible to set electrification time of normal rotation and reversal rotation freely individually between 1[msec]~63[msec] and by 1[msec] step.
When shipping it is set (T1=14 and T2=14)
- ② The trigger input circuit is insulated at the photo-coupler, furthermore because power source "V1,V2" for the solenoid and circuit power source "Vcc" is independent completely, it can make the interface of the peripheral circuit easy.
- ③ Because we use "C-MOS-IC" for counter/logic circuit, there are some features such as circuit electric power consumption (Vcc-GND2) is small, noise room is large, and the circuit power source voltage range is wide.
- ④ Because the both sides of the solenoid drive circuit substrate are coated with modacrylic coating material, moisture-resistant quality, shock resistant quality and vibration resistant quality are at satisfactory level.

■Specification

Rated voltage

- ① Solenoid drive power source (H) V1-GND1 :
DC12V~DC48V
- ② Solenoid drive power source (L) V2-GND1 :
DC12V~DC48V (V1>V2)
- ③ Circuit power source Vcc-GND2 : DC5V±10%

Circuit power source electric current

- ① Circuit power source electric current Icc 2 :
under 30mA (Vcc-GND2)

Rated output current

- ① Solenoid output current (at the time of pulse train operation)
 - V1,V2=48V : Within 1.2A (Ra=44 Ohm)
 - V1,V2=24V : Within 2.0A (Ra=12 Ohm)
 - V1,V2=12V : Within 1.2A
- ② Solenoid output current (continual retention operation)
 - V2=12~48V : within 1.2A

Trigger In

- ① High level input Voltage V_{IH} : DC12V~DC48V
(DC50V MAX)
- ② Low level input Voltage V_{IL} : DC0V~DC1.2V

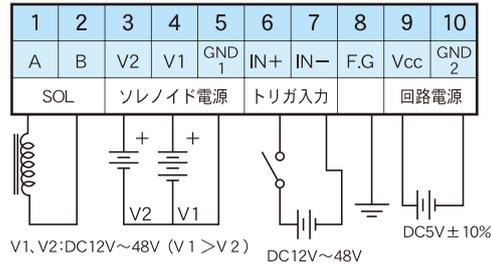
3、触发输入电流 I_{IH} : DC7.5mA(Typ)
(at. V_{IH} =DC48V)

通电时间设定

- 1、正方向通电时间T1 (A-B)
1ms~63ms(1ms-STEP)
(标准通电时间T1=14ms)
- 2、反方向通电时间T2(B-A)
1ms~63ms(1ms-STEP)
(标准通电时间T2=14ms)

結線方法

端子台 (10P): "BP101S-10" (和泉電気)
[結線方法]



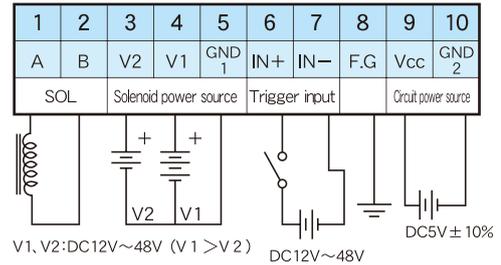
③ Trigger input electric current I_{IH} : DC7.5mA (Typ)
(at. V_{IH} =48V)

Electrification time setting

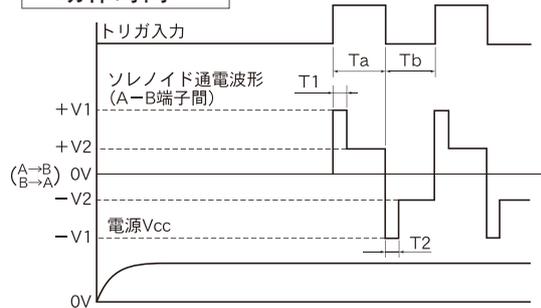
- ① Forward electrification time T1 (A->B):
1 msec~63msec (1msec-STEP)
(Standard electrification time T1=14msec)
- ② Opposite direction electrification time T2 (B->A):
1 msec~63 msec (1msec-STEP)
(Standard electrification time T2=14msec)

Wire connection method

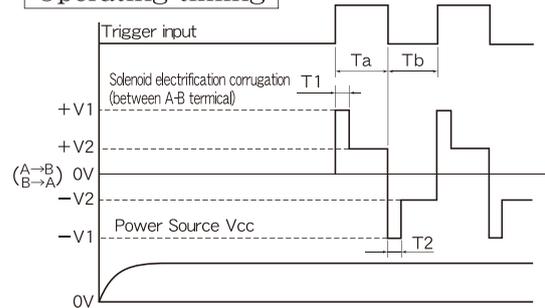
Terminal Block (10P): "BP1015-10" (Izumi Denki)
Wire connection method



動作時間



Operating timing



外形尺寸图/Outside dimension

